FIG. 1

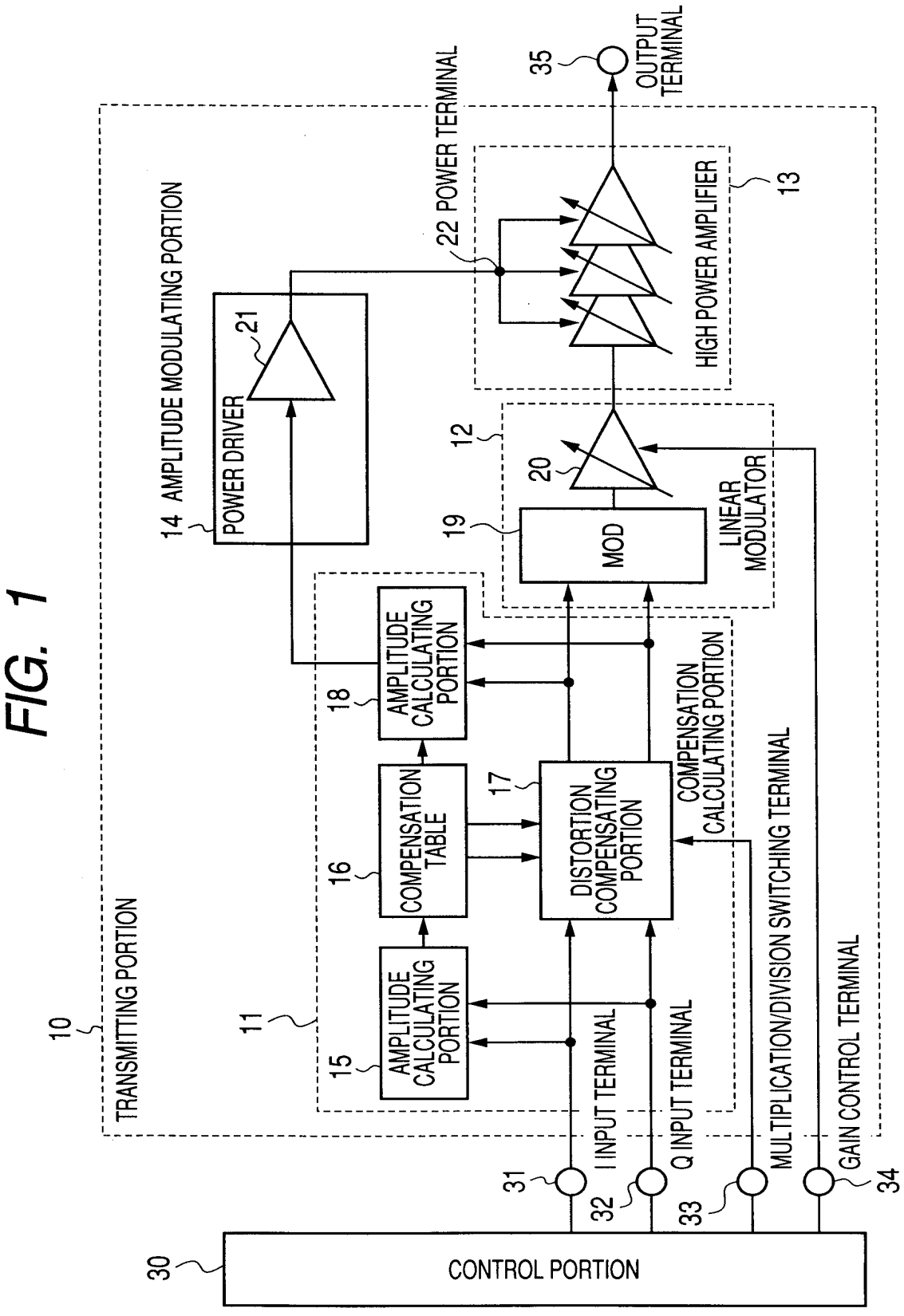

10
TRANSMITTING PORTION
11
14 AMPLITUDE MODULATING PORTION
POWER DRIVER
21
22 POWER TERMINAL
35
OUTPUT TERMINAL
HIGH POWER AMPLIFIER
13
12
20
19
MOD
LINEAR MODULATOR
18
AMPLITUDE CALCULATING PORTION
16
COMPENSATION TABLE
17
DISTORTION COMPENSATING PORTION
COMPENSATION CALCULATING PORTION
15
AMPLITUDE CALCULATING PORTION
31
I INPUT TERMINAL
32
Q INPUT TERMINAL
33
MULTIPLICATION/DIVISION SWITCHING TERMINAL
34
GAIN CONTROL TERMINAL
30
CONTROL PORTION

FIG. 2

INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —

FIG. 3

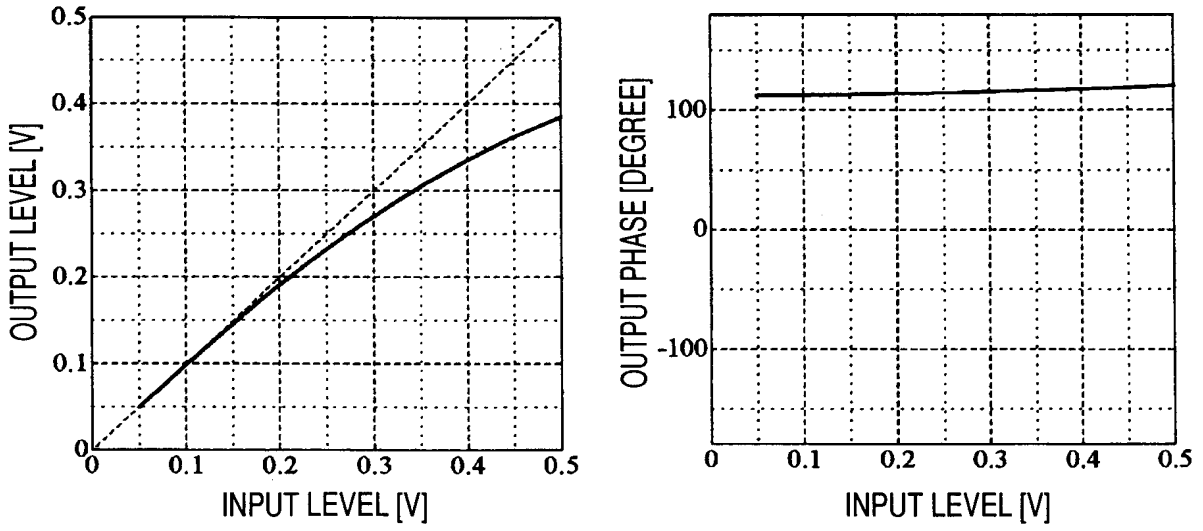


FIG. 4

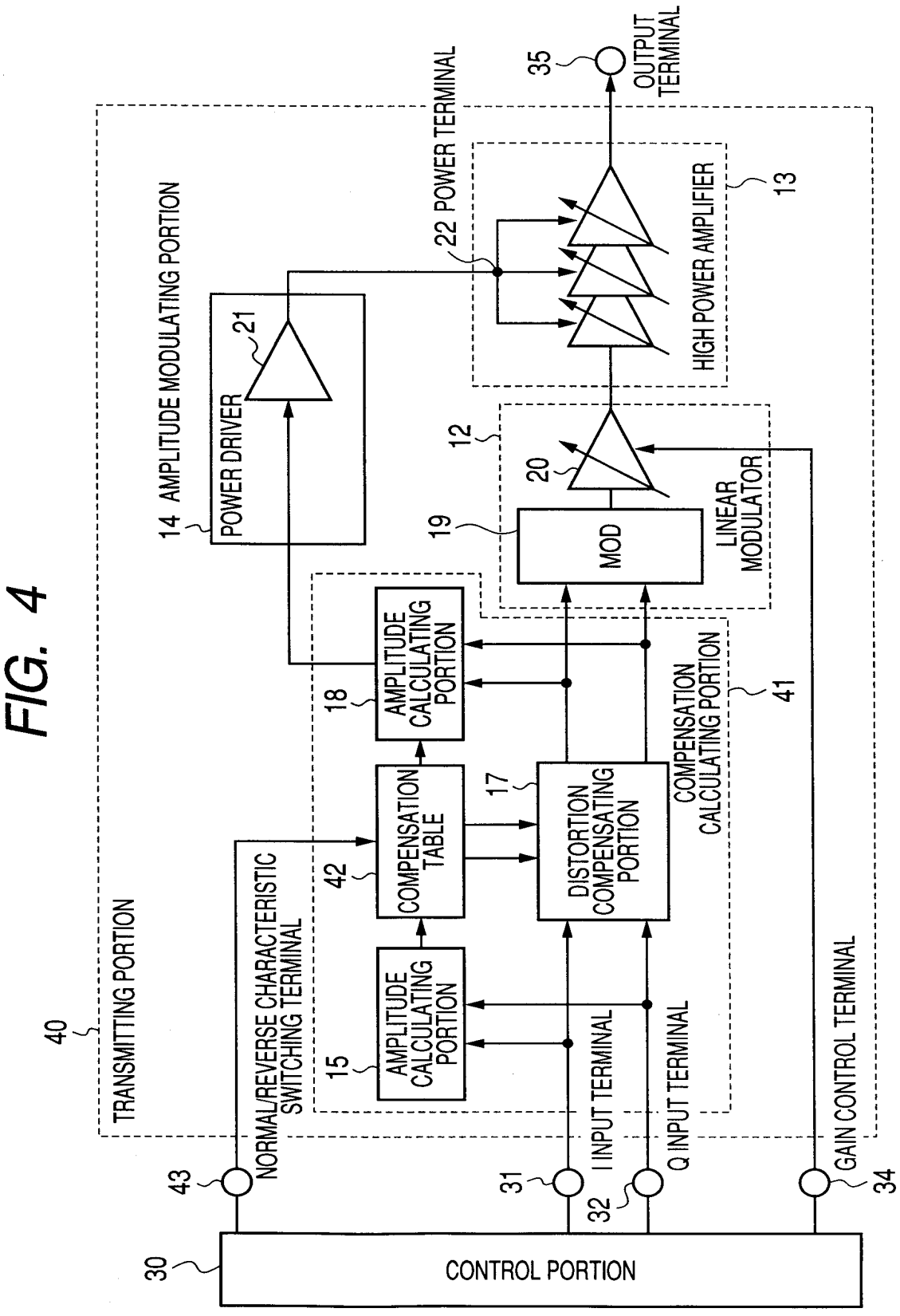

40
TRANSMITTING PORTION
14 AMPLITUDE MODULATING PORTION
POWER DRIVER
21
22 POWER TERMINAL
35
OUTPUT TERMINAL
HIGH POWER AMPLIFIER
13
12
20
19
MOD
LINEAR MODULATOR
18
AMPLITUDE CALCULATING PORTION
COMPENSATION CALCULATING PORTION
41
42
COMPENSATION TABLE
17
DISTORTION COMPENSATING PORTION
15
AMPLITUDE CALCULATING PORTION
NORMAL/REVERSE CHARACTERISTIC SWITCHING TERMINAL
43
I INPUT TERMINAL
31
Q INPUT TERMINAL
32
GAIN CONTROL TERMINAL
34
30
CONTROL PORTION

FIG. 5

NORMAL CHARACTERISTIC		
INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —
REVERSE CHARACTERISTIC		
INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)	DISTORTION DATA (PHASE)
— — —	— — —	— — —

FIG. 6(A)

NORMAL CHARACTERISTIC

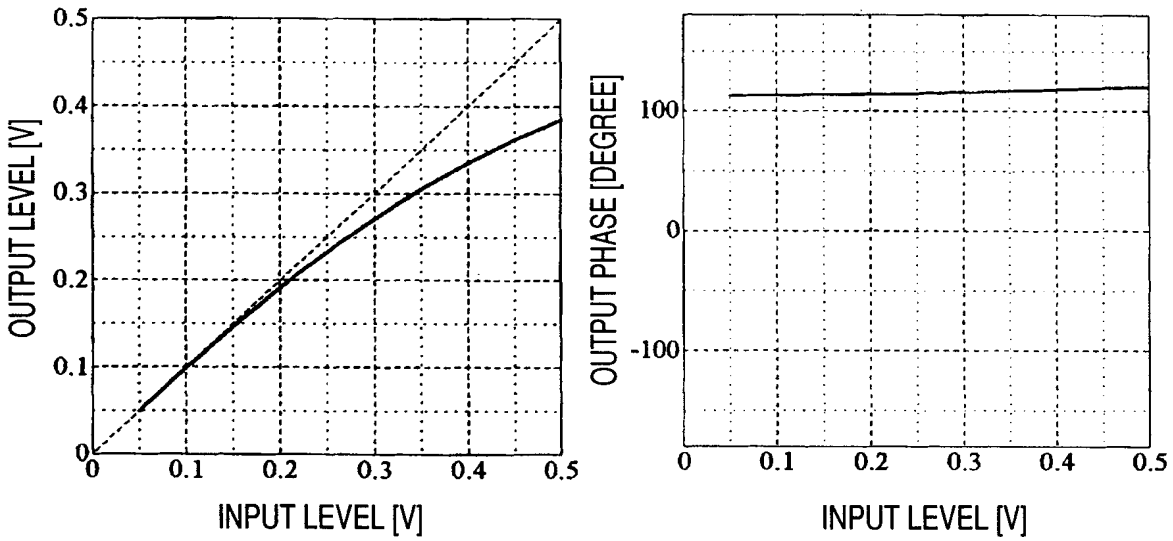


FIG. 6(B)

REVERSE CHARACTERISTIC

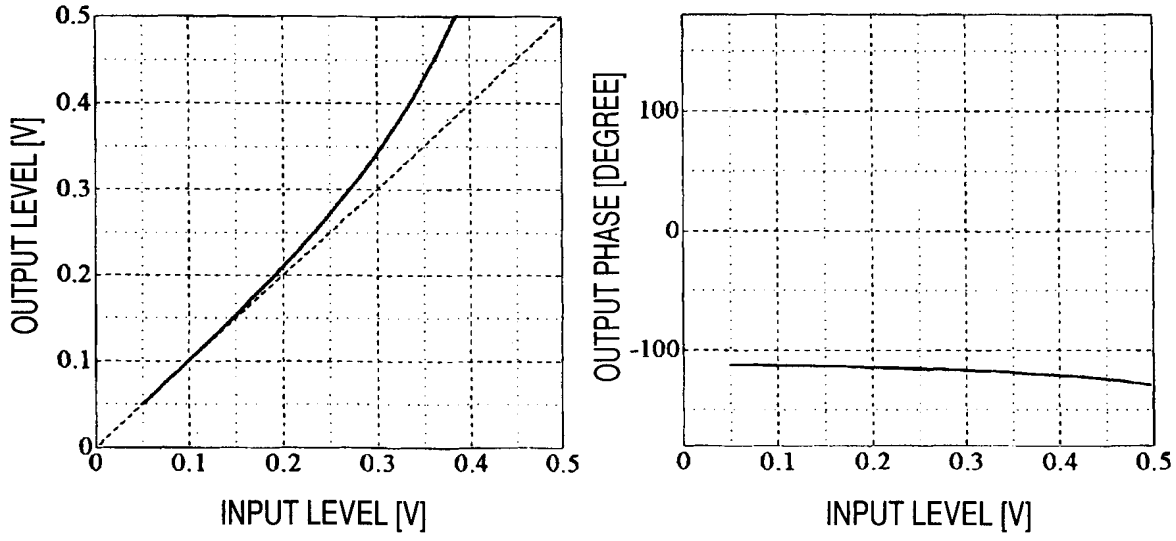


FIG. 7

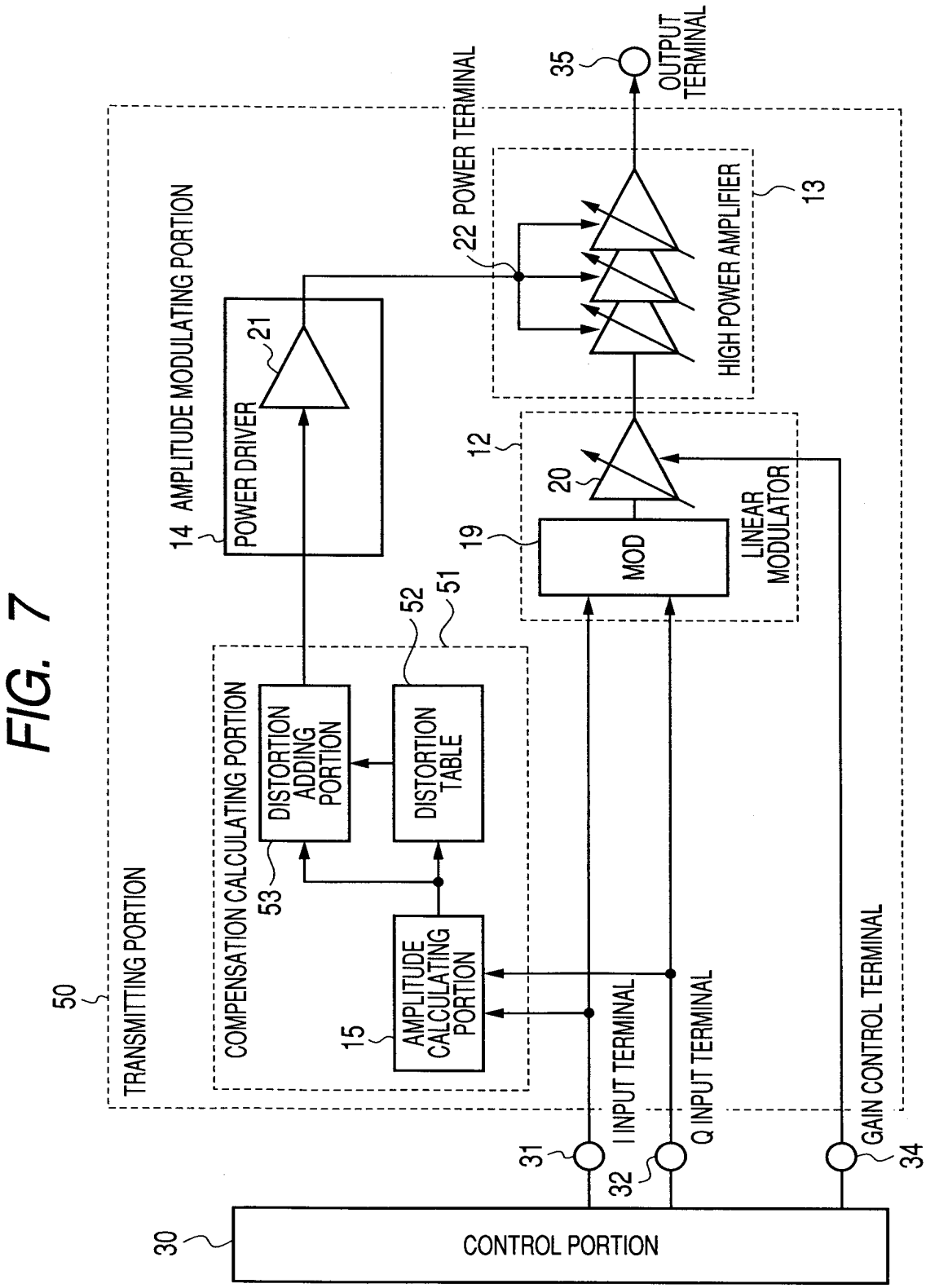

50
TRANSMITTING PORTION
14 AMPLITUDE MODULATING PORTION
POWER DRIVER
21
22 POWER TERMINAL
35
OUTPUT TERMINAL
HIGH POWER AMPLIFIER
13
12
20
19
MOD
LINEAR MODULATOR
COMPENSATION CALCULATING PORTION
51
52
DISTORTION ADDING PORTION
DISTORTION TABLE
53
15
AMPLITUDE CALCULATING PORTION
31
I INPUT TERMINAL
32
Q INPUT TERMINAL
34
GAIN CONTROL TERMINAL
30
CONTROL PORTION

FIG. 8

INPUT LEVEL	DISTORTION DATA (OUTPUT LEVEL)
— — —	— — —

FIG. 9

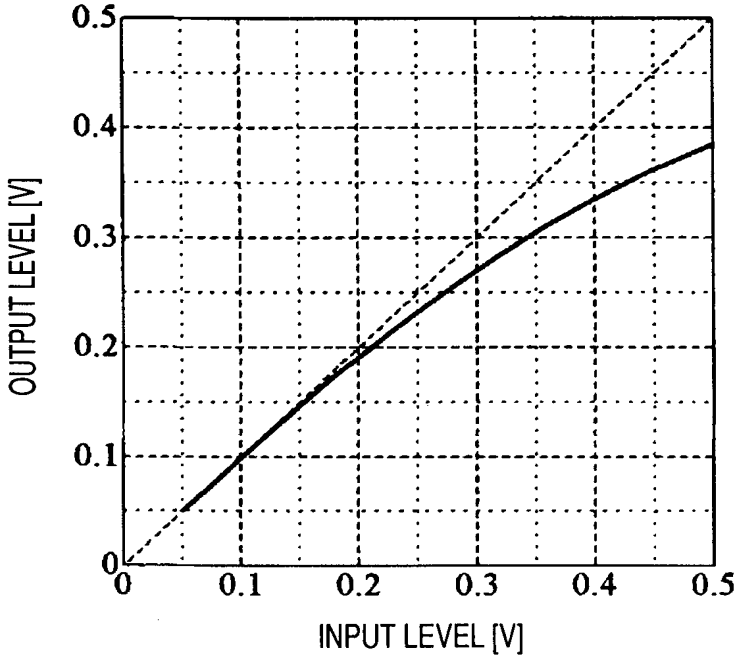


FIG. 10

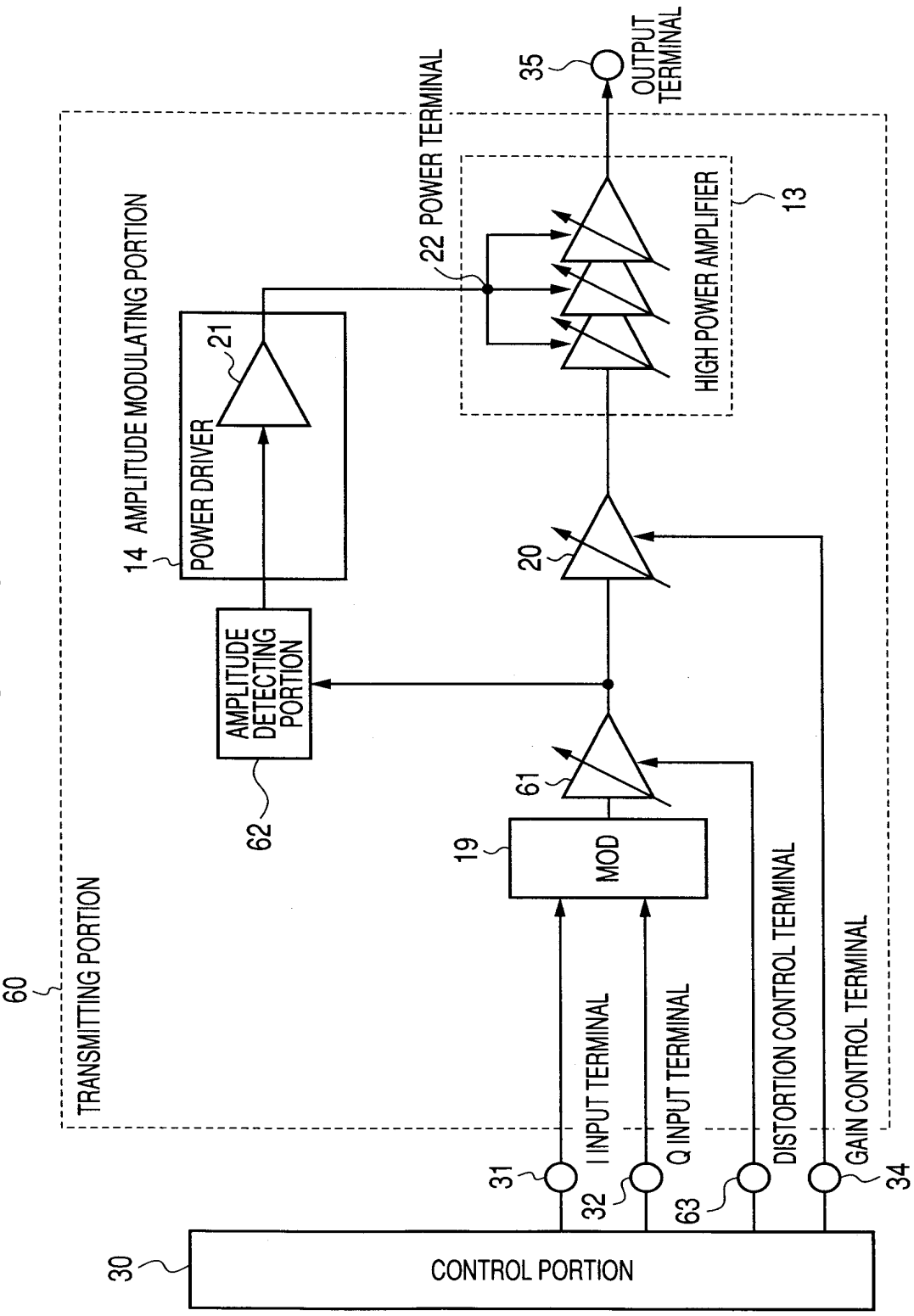

60
TRANSMITTING PORTION
30
CONTROL PORTION
31
I INPUT TERMINAL
32
Q INPUT TERMINAL
63
DISTORTION CONTROL TERMINAL
34
GAIN CONTROL TERMINAL
19
MOD
61
62
AMPLITUDE DETECTING PORTION
14
AMPLITUDE MODULATING PORTION
POWER DRIVER
21
20
22
POWER TERMINAL
HIGH POWER AMPLIFIER
13
35
OUTPUT TERMINAL

FIG. 11

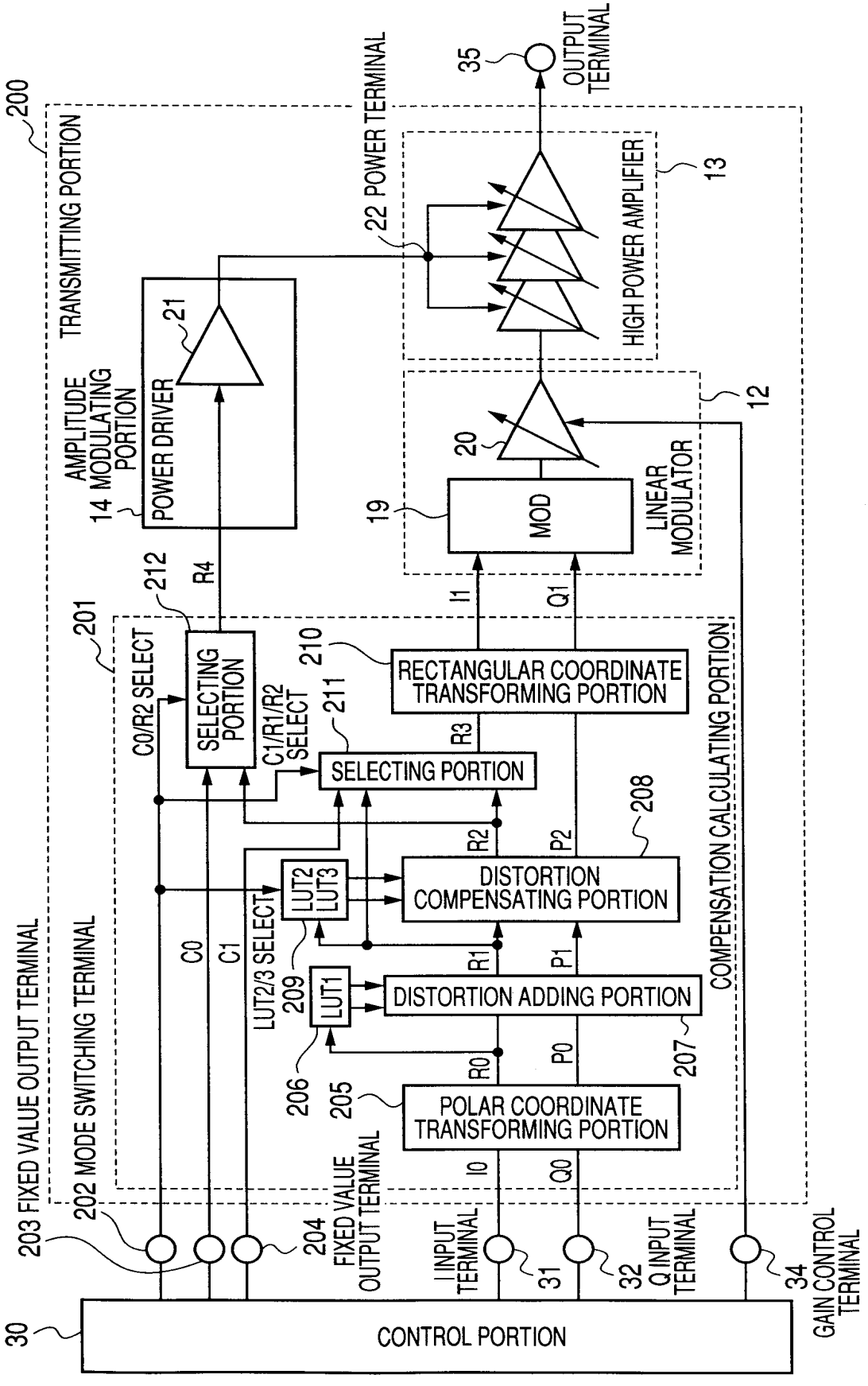
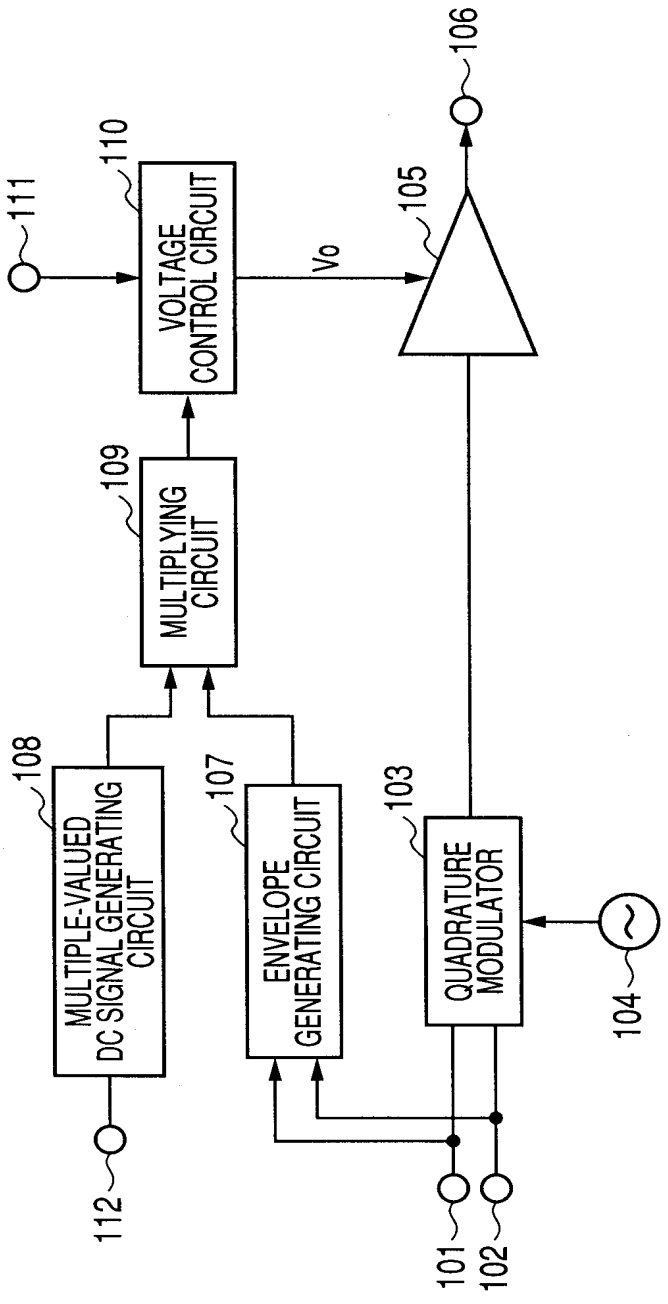

200
TRANSMITTING PORTION
AMPLITUDE MODULATING PORTION
14
POWER DRIVER
21
22 POWER TERMINAL
HIGH POWER AMPLIFIER
13
35
OUTPUT TERMINAL
12
LINEAR MODULATOR
20
19
MOD
I1
Q1
201
212
R4
C0/R2 SELECT
SELECTING PORTION
C1/R1/R2 SELECT
211
210
RECTANGULAR COORDINATE TRANSFORMING PORTION
R3
SELECTING PORTION
208
R2
P2
LUT2
LUT3
DISTORTION COMPENSATING PORTION
C0
C1
LUT2/3 SELECT
209
LUT1
R1
P1
DISTORTION ADDING PORTION
COMPENSATION CALCULATING PORTION
207
R0
P0
206
205
POLAR COORDINATE TRANSFORMING PORTION
I0
Q0
203 FIXED VALUE OUTPUT TERMINAL
202 MODE SWITCHING TERMINAL
204 FIXED VALUE OUTPUT TERMINAL
I INPUT TERMINAL
31
32 Q INPUT TERMINAL
34 GAIN CONTROL TERMINAL
30
CONTROL PORTION

FIG. 12


MULTIPLE-VALUED DC SIGNAL GENERATING CIRCUIT
ENVELOPE GENERATING CIRCUIT
MULTIPLYING CIRCUIT
VOLTAGE CONTROL CIRCUIT
QUADRATURE MODULATOR
Vo
101
102
103
104
105
106
107
108
109
110
111
112